

FIG. 1

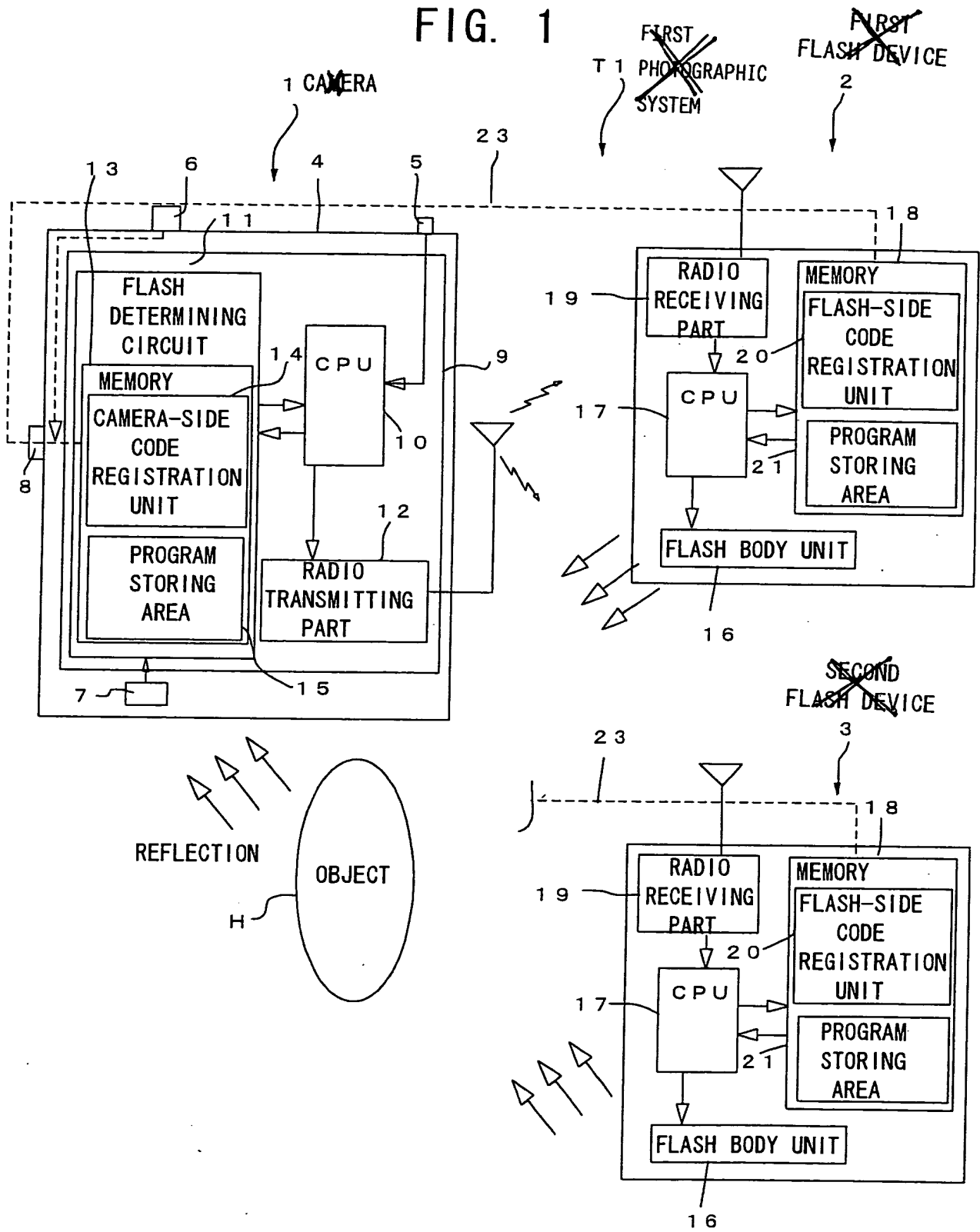


FIG. 5

PRELIMINARY FLASHING

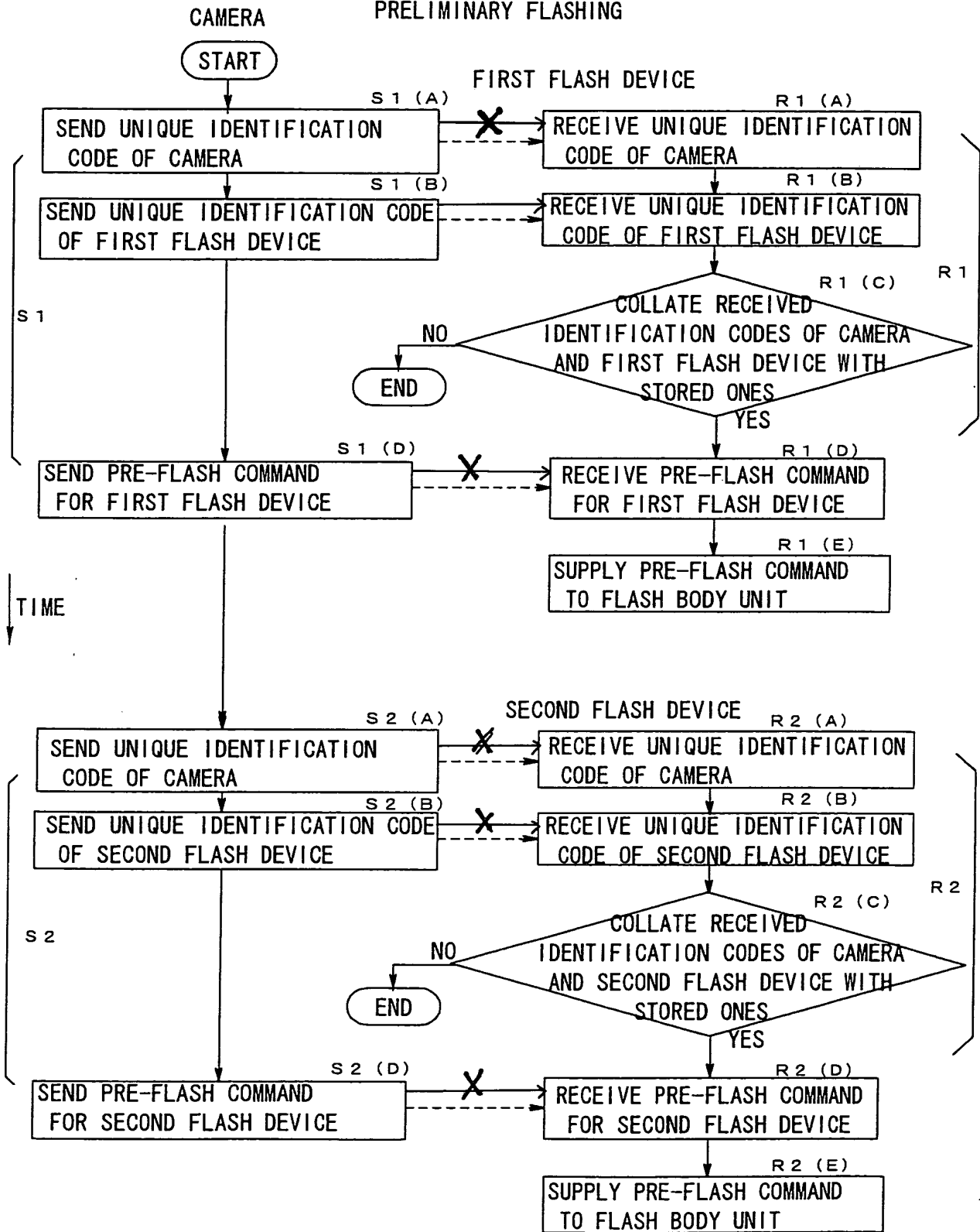


FIG. 6  
MAIN FLASHING

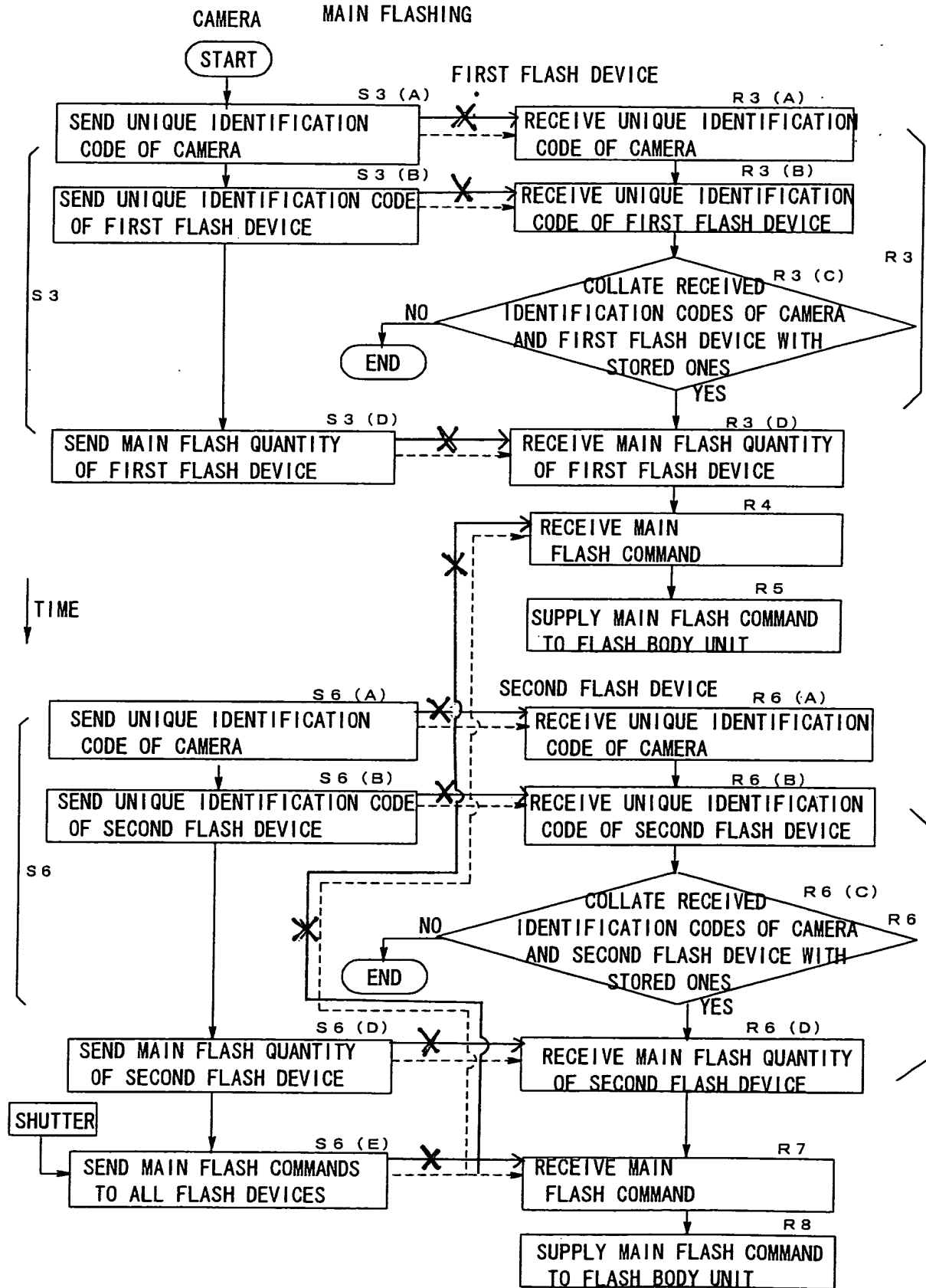


FIG. 9

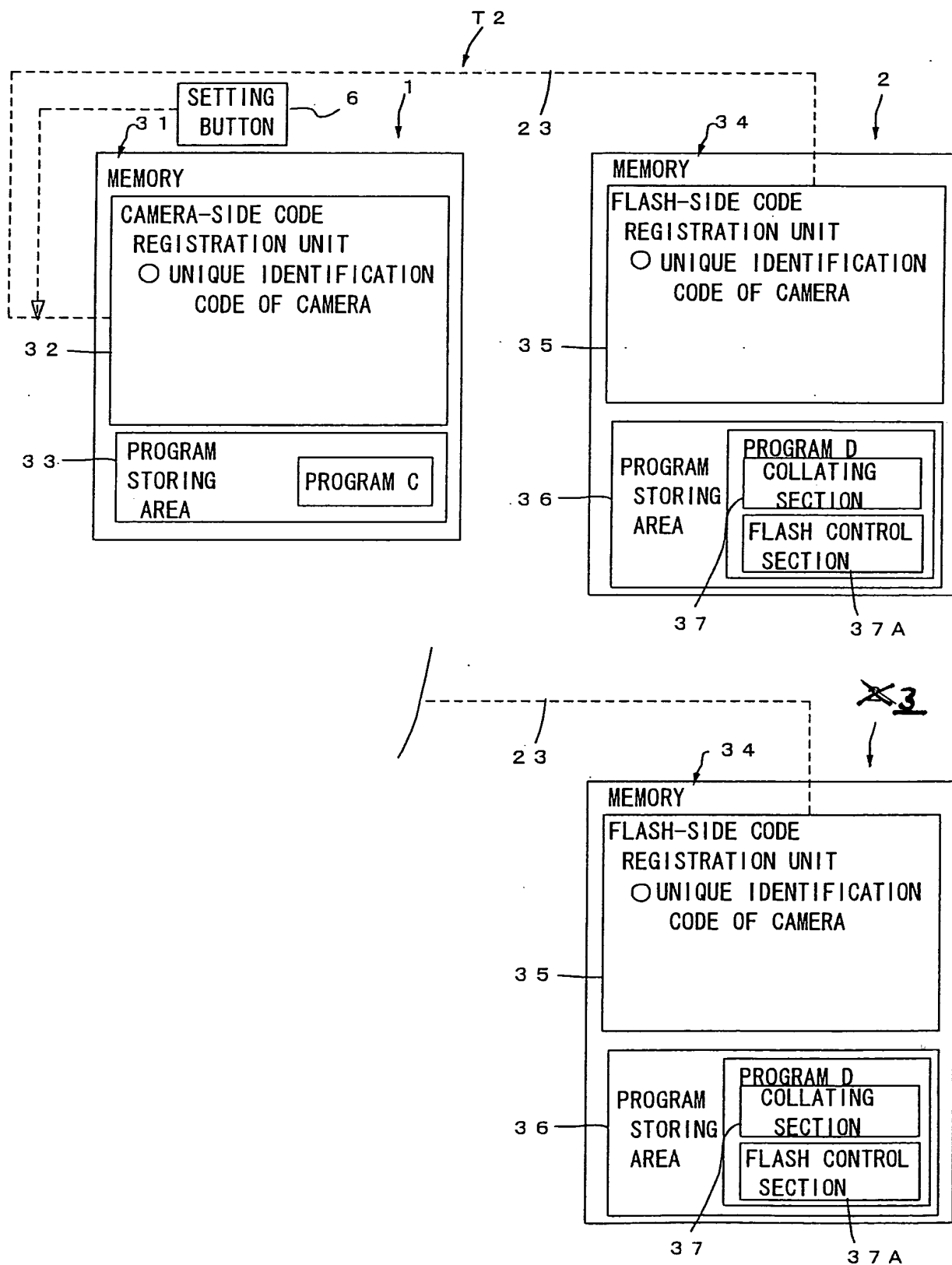


FIG. 11

PRELIMINARY FLASHING

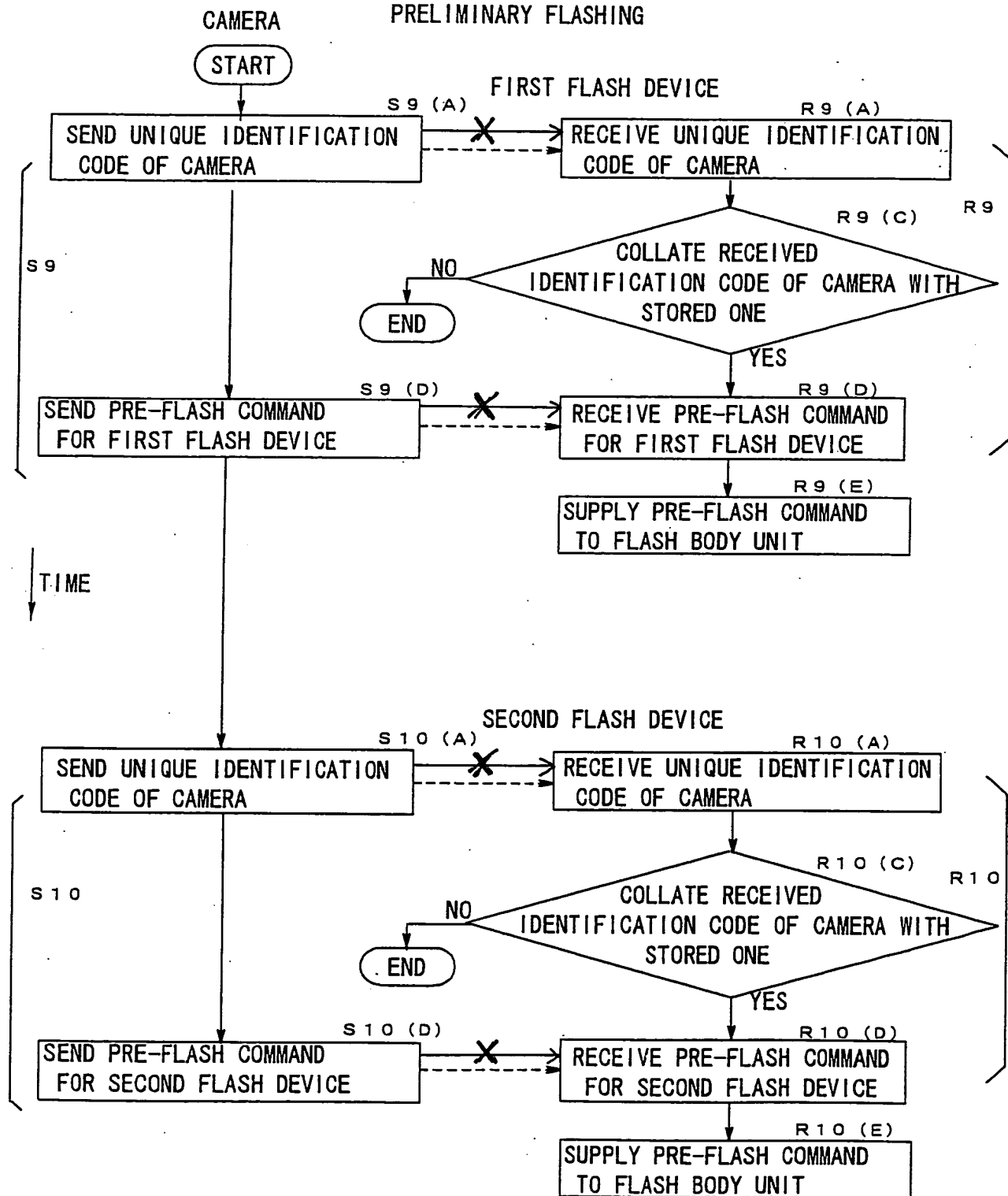


FIG. 12

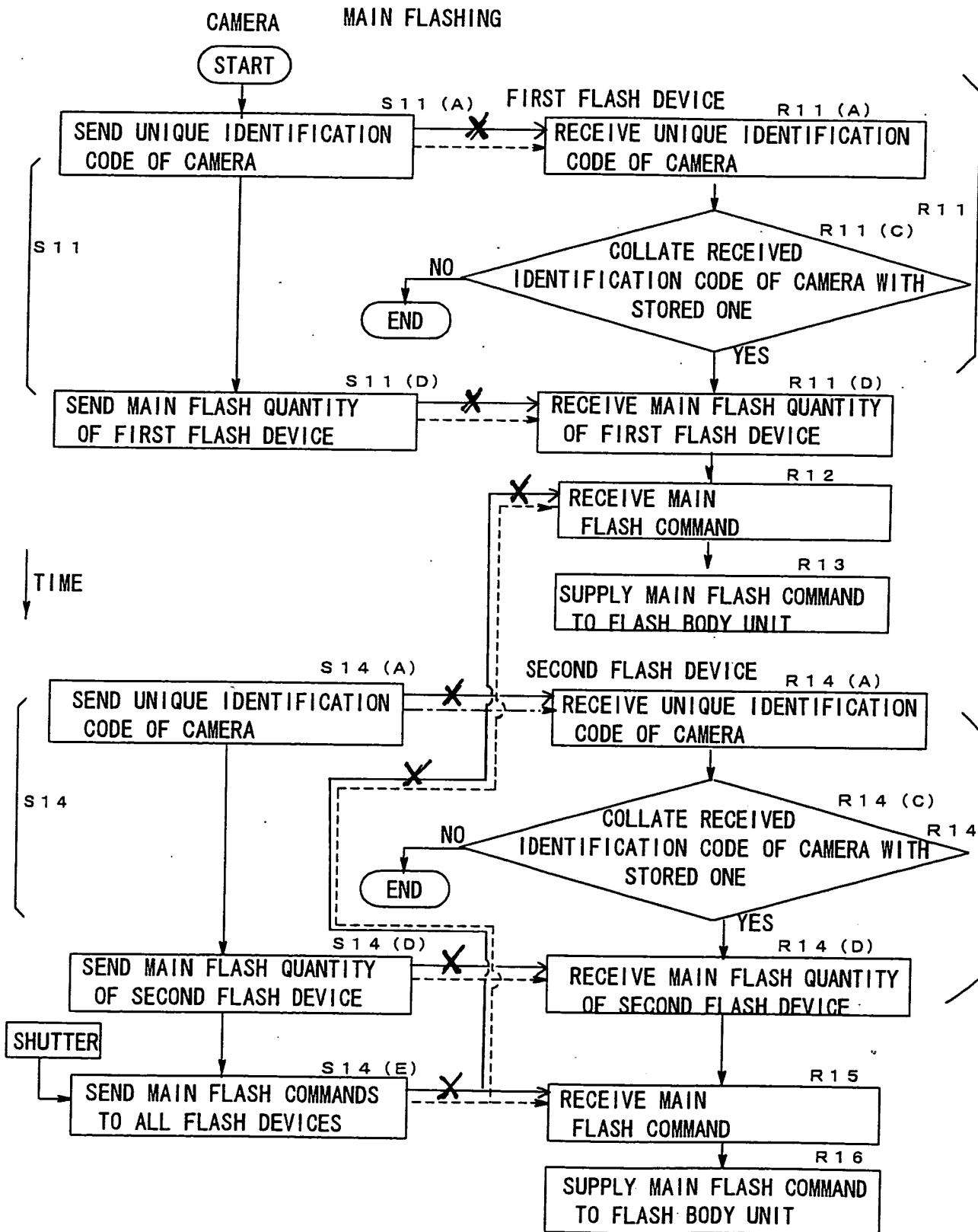


FIG. 14

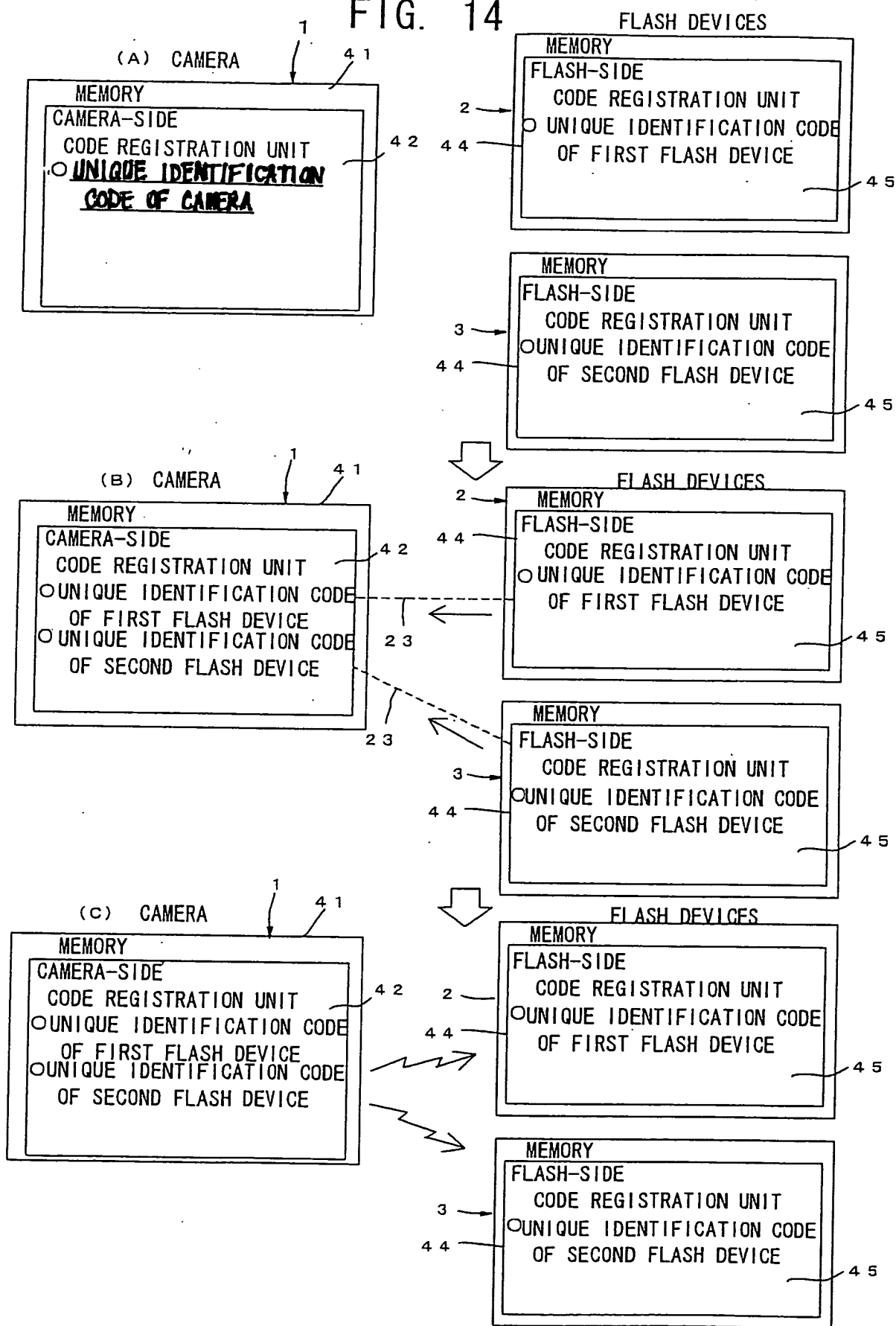


FIG. 15  
PRELIMINARY FLASHING

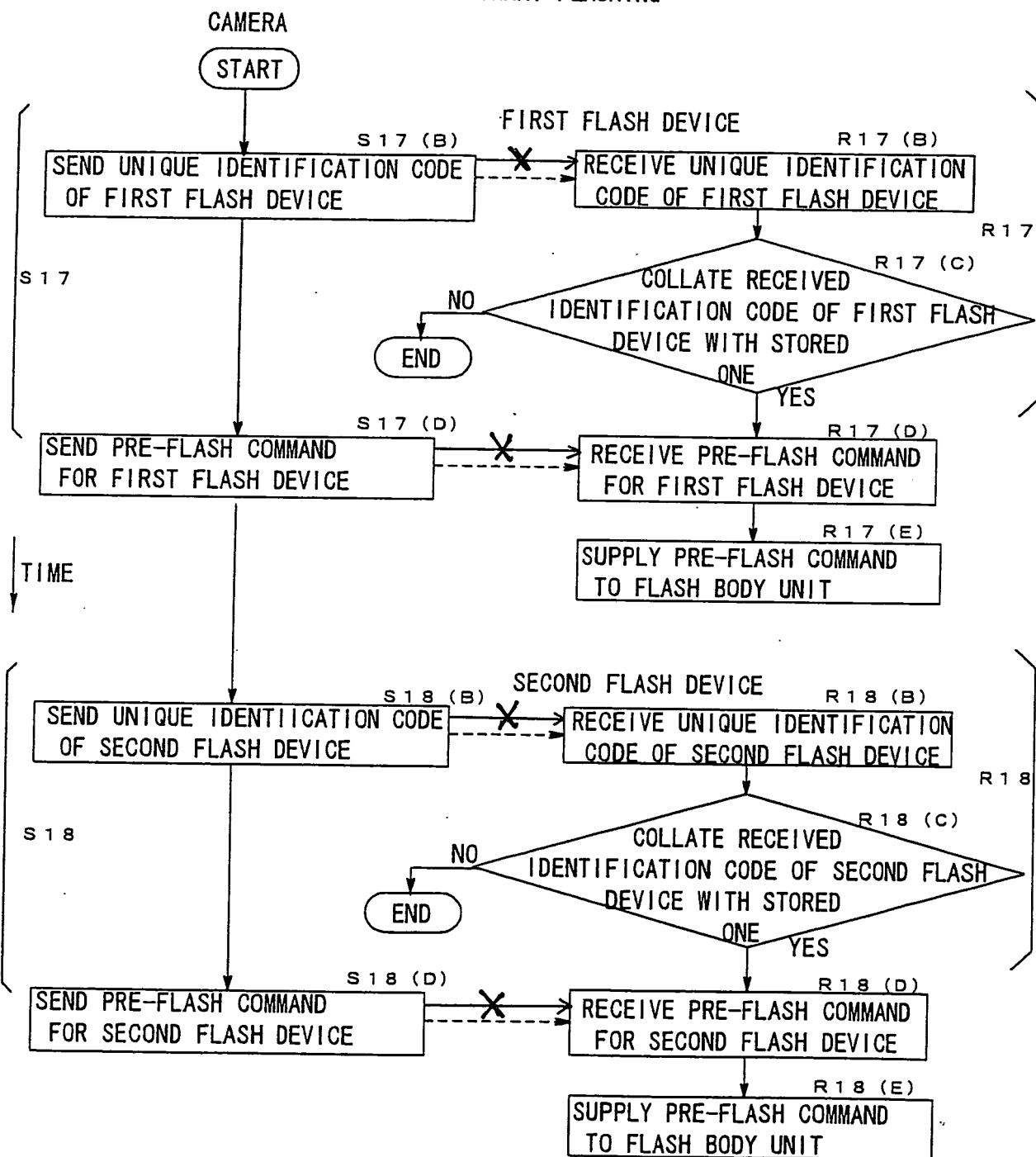




FIG. 16

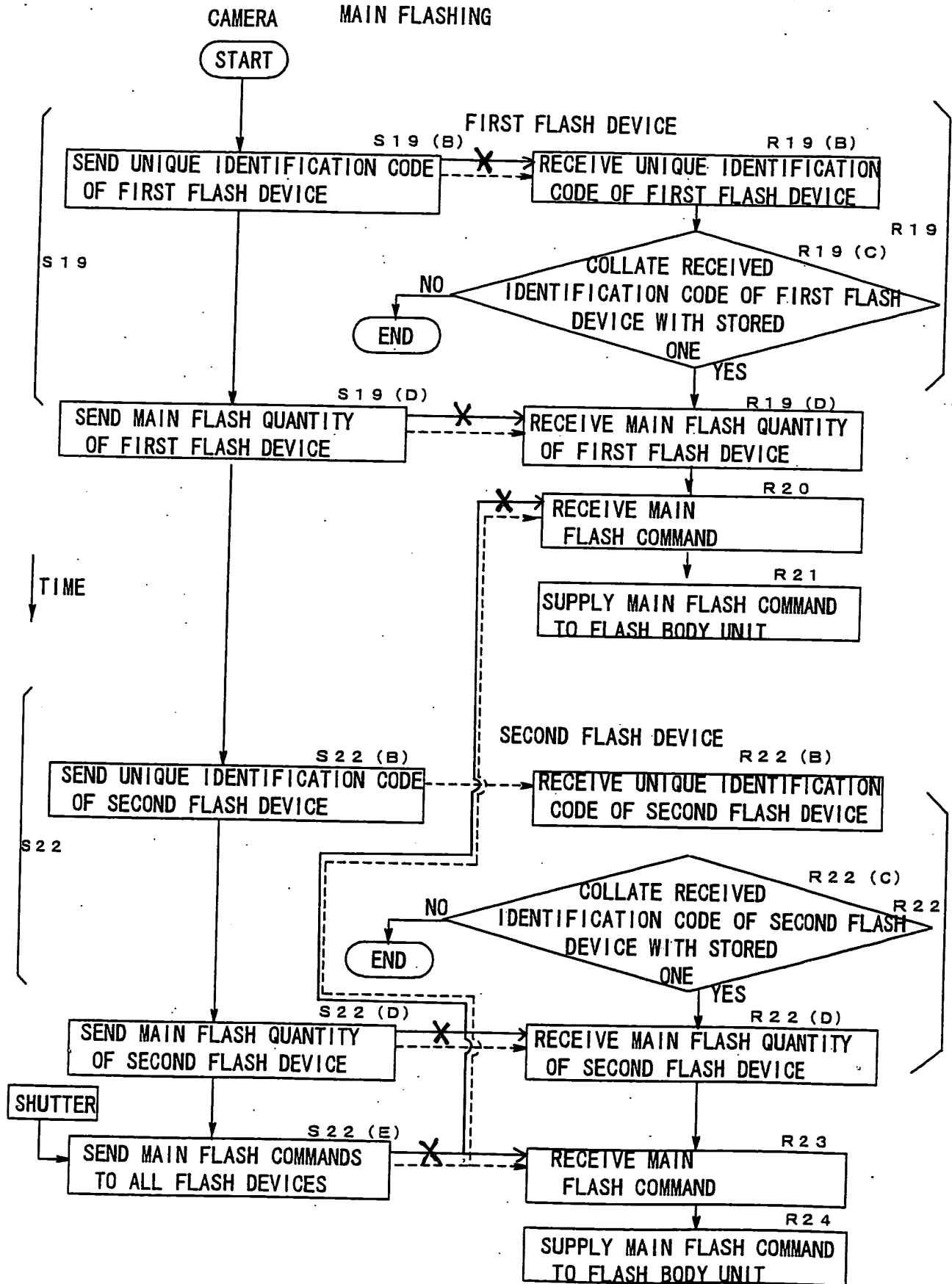


FIG. 20  
PRELIMINARY FLASHING

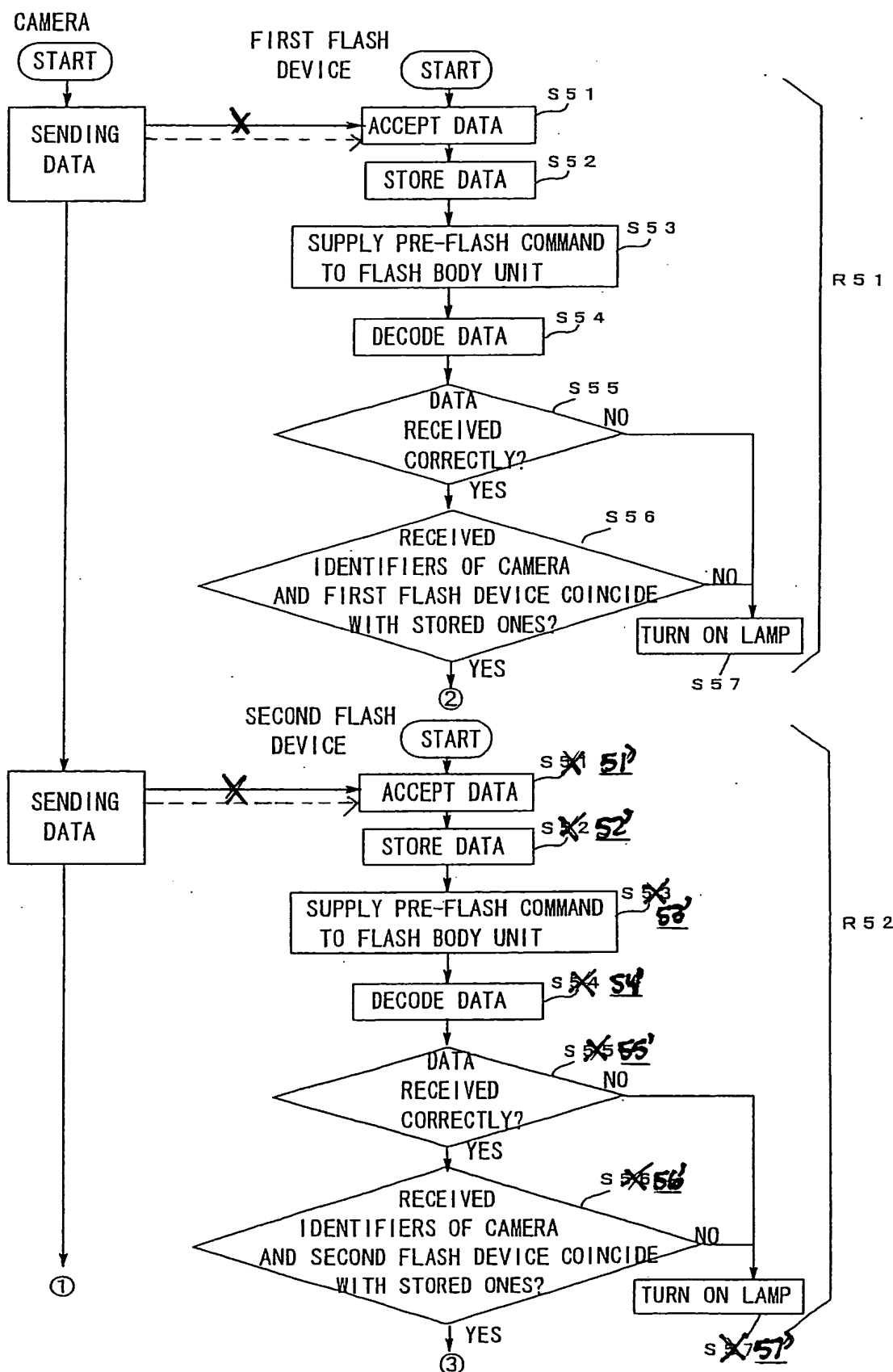


FIG. 17

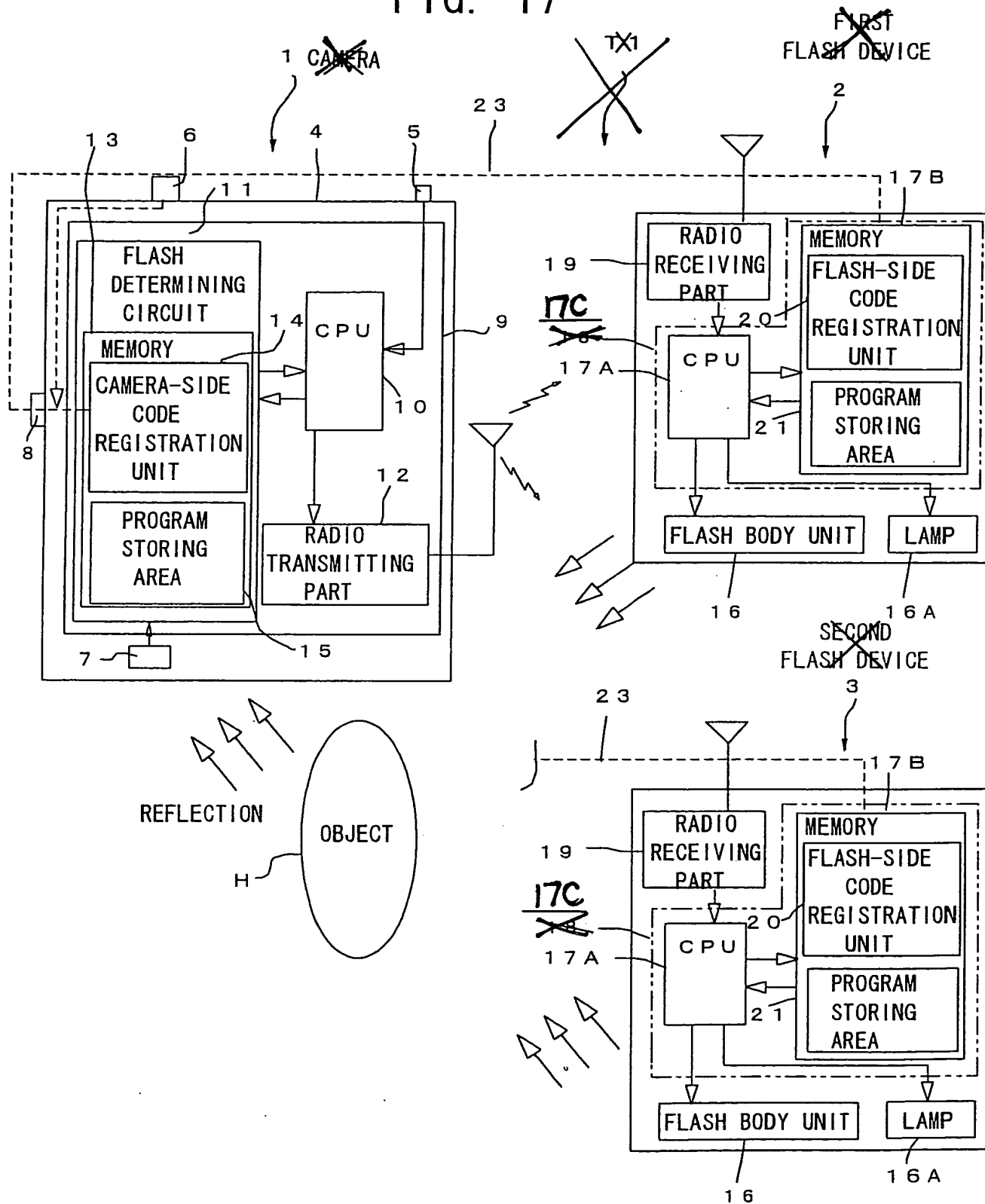


FIG. 21  
MAIN FLASHING

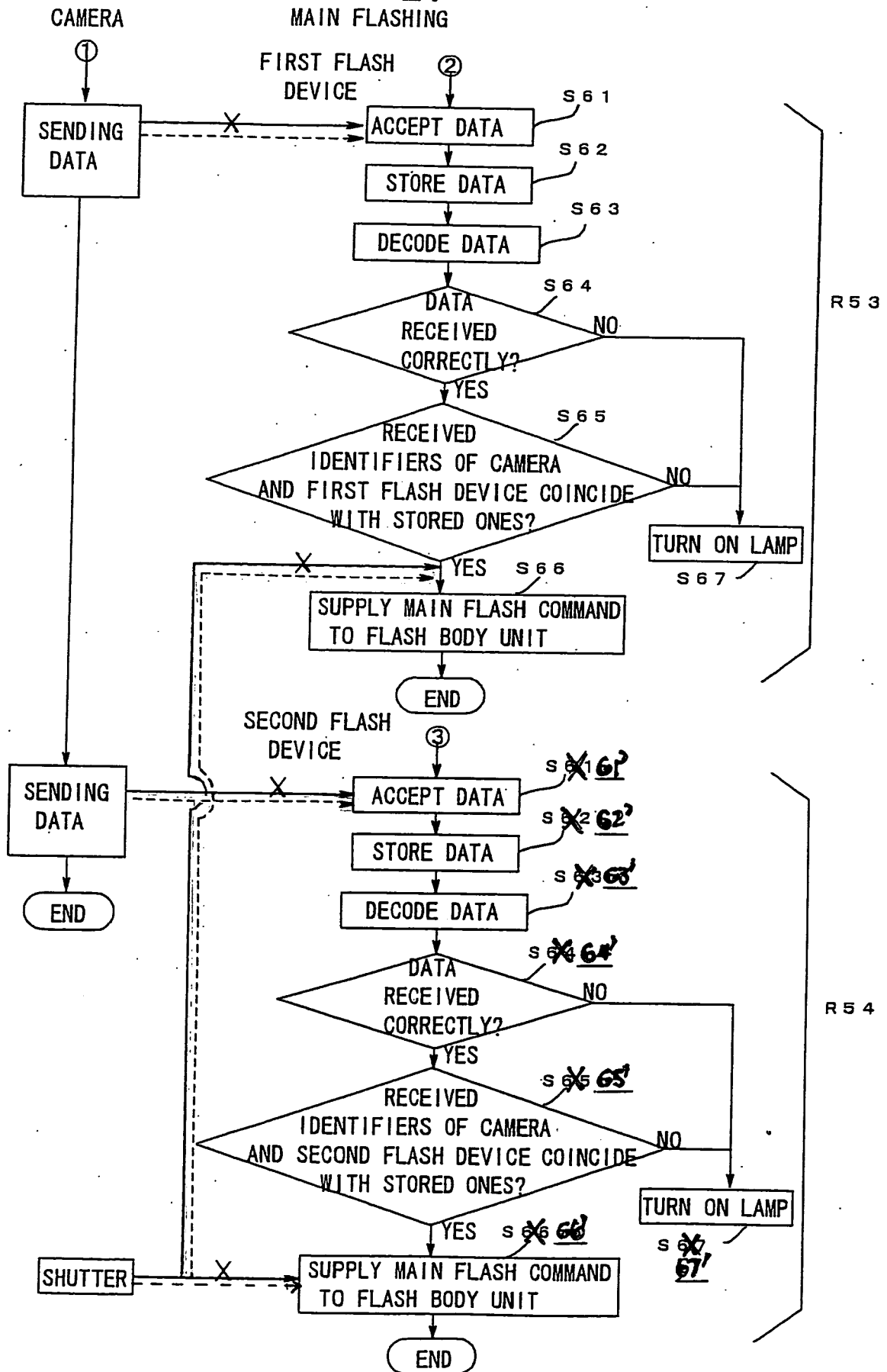


FIG. 23 Prior Art

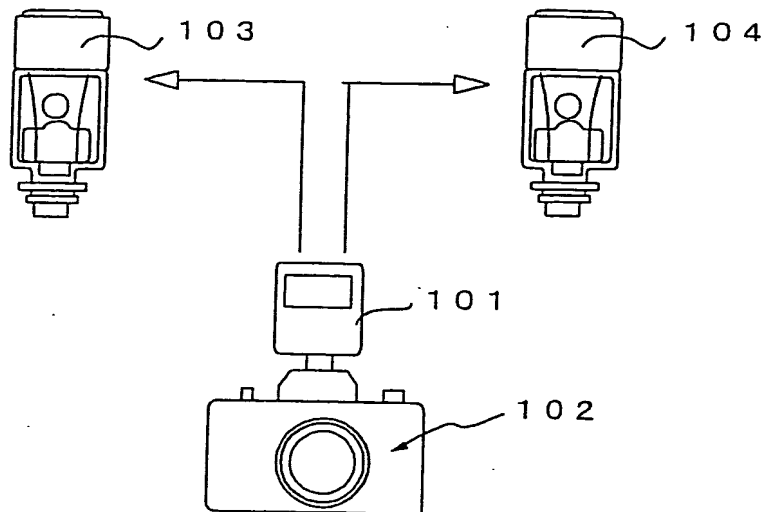


FIG. 24 Prior Art

PRELIMINARY FLASHING

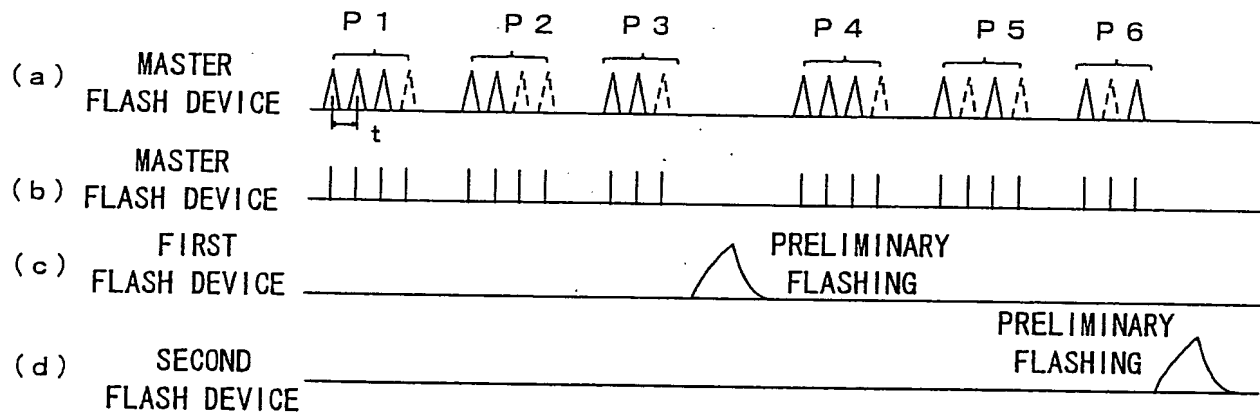


FIG. 25 Prior Art

SIGNAL TO MAIN FLASHING

FIRST FLASH SIGNAL TO  
DEVICE SECOND  
FLASH DEVICE

